

	Search Text
1	(crosstalk\$5 cross adj talk\$4) and @ad<"20021120"
2	(crosstalk\$5 cross adj talk\$4) and @ad<"20021120" and (wirelength (wir\$4 rout\$4) adj length) with (infring\$6 violat\$4 constrain\$4 meet\$4 meet error)
3	(crosstalk\$5 cross adj talk\$4 or (coupl\$5 near2 noise)) and @ad<"20021120" and (wirelength (wir\$4 rout\$4) adj length) with (infring\$6 violat\$4 constrain\$4 meet\$4 meet error) and (buffer driver inverter) with (plac\$6 insert\$4 add\$7 creat\$4 layout\$4 arrang\$4)
4	(crosstalk\$5 cross adj talk\$4) and @ad<"20021120" and (wirelength (wir\$4 rout\$4) adj length) with (infring\$6 violat\$4 constrain\$4 meet\$4 meet error) and (buffer driver inverter) with (plac\$6 insert\$4 add\$7 creat\$4 layout\$4 arrang\$4)
5	(crosstalk\$5 cross adj talk\$4) and @ad<"20021120" and (wirelength (wir\$4 rout\$4) adj length) with (infring\$6 violat\$4 constrain\$4 meet\$4 meet error) and (buffer driver inverter) with (extract\$4 divi\$6 plac\$6 insert\$4 add\$7 creat\$4 layout\$4 arrang\$4)
6	(crosstalk\$5 cross adj talk\$4 or (coupl\$5 near2 noise)) and @ad<"20021120" and (wirelength (wir\$4 rout\$4) adj length) with (infring\$6 violat\$4 satisf\$7 constrain\$4 meet\$4 meet error budget) and (buffer driver inverter) with (divi\$5 plac\$6 insert\$4 add\$7 creat\$4 layout\$4 arrang\$4) and (search\$5 find\$4 look\$5) same (empty available) same (area section portion spac\$4 part)
7	(crosstalk\$5 cross adj talk\$4 or (coupl\$5 near2 noise)) and @ad<"20021120" and (wirelength (wir\$4 rout\$4) adj length) with (infring\$6 violat\$4 satisf\$7 constrain\$4 meet\$4 meet error budget) and (buffer driver inverter) with (plac\$6 insert\$4 add\$7 creat\$4 layout\$4 arrang\$4)
8	(crosstalk\$5 cross adj talk\$4 or (coupl\$5 near2 noise)) and @ad<"20021120" and (wirelength (wir\$4 rout\$4) adj length) with (infring\$6 violat\$4 satisf\$7 constrain\$4 meet\$4 meet error budget) and (buffer driver inverter) with (divi\$5 plac\$6 insert\$4 add\$7 creat\$4 layout\$4 arrang\$4) and (search\$5 find\$4 look\$5 check\$4) same (empty available) same (area section portion spac\$4 part)
9	(crosstalk\$5 cross adj talk\$4 or (coupl\$5 near2 noise)) and @ad<"20021120" and (wirelength (wir\$4 rout\$4) adj length) with (infring\$6 violat\$4 satisf\$7 constrain\$4 meet\$4 meet error budget) and (buffer driver inverter) with (divi\$5 plac\$6 insert\$4 add\$7 creat\$4 layout\$4 arrang\$4) and (search\$5 find\$4 look\$5 check\$4) same (empty available) same (area region section portion spac\$4 part)
10	(crosstalk\$5 cross adj talk\$4 or (coupl\$5 near2 noise)) and @ad<"20021120" and (wirelength (wir\$4 rout\$4) adj length) with (infring\$6 violat\$4 satisf\$7 constrain\$4 meet\$4 meet error budget) and (buffer driver inverter) with (divi\$5 plac\$6 insert\$4 add\$7 creat\$4 layout\$4 arrang\$4) and (search\$5 find\$4 look\$5 check\$4) same (area region section portion spac\$4 part)